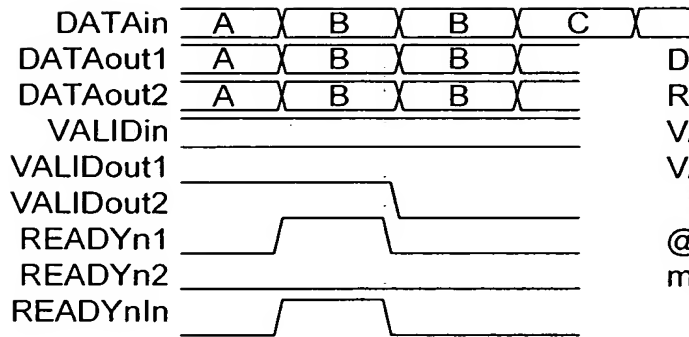


FIG. 1

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DATAout1 = DATAout2 = DATAin  
 READYnln = READYn1 / READYn2  
 VALIDout1 = VALIDin & ! mask1  
 VALIDout2 = VALIDin & ! mask2

@ Posedge clock  
 $mask1 \leftarrow (READYn2 \& VALIDout\ 2) \& !(READYn1 \& VALIDout\ 1)$   
 $mask2 \leftarrow (READYn1 \& VALIDout\ 1) \& !(READYn2 \& VALIDout\ 2)$

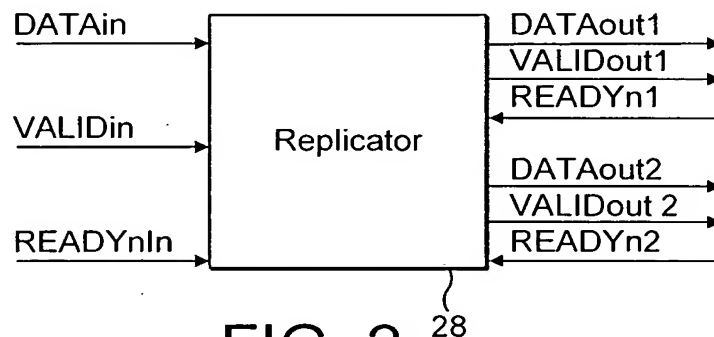


FIG. 2

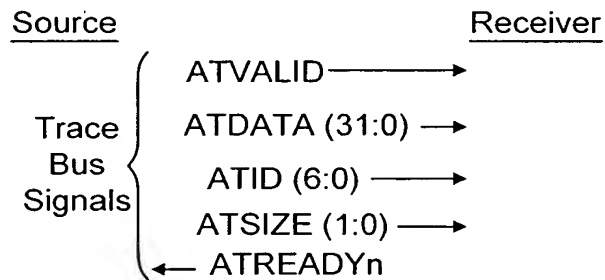


FIG. 3

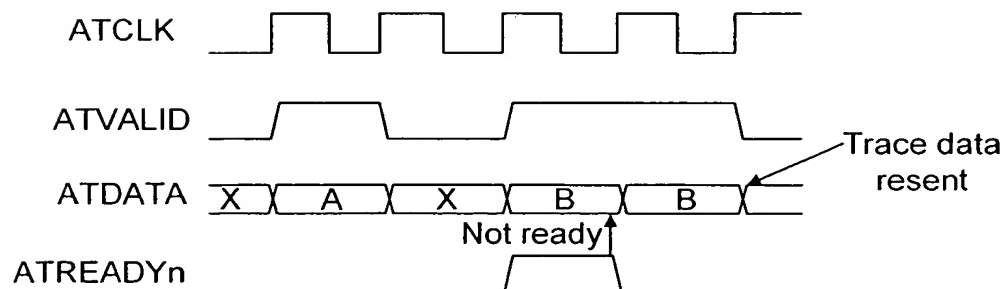


FIG. 4

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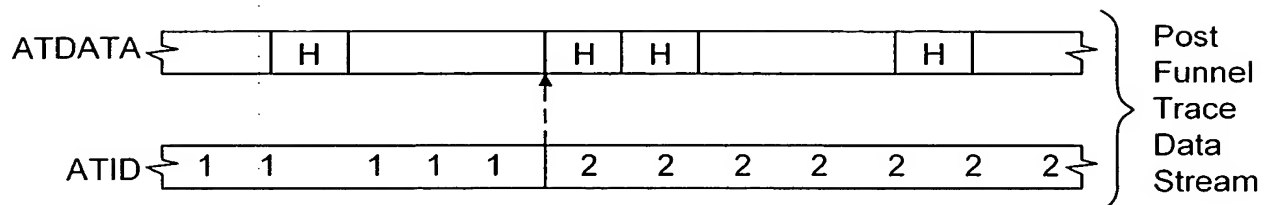


FIG. 5

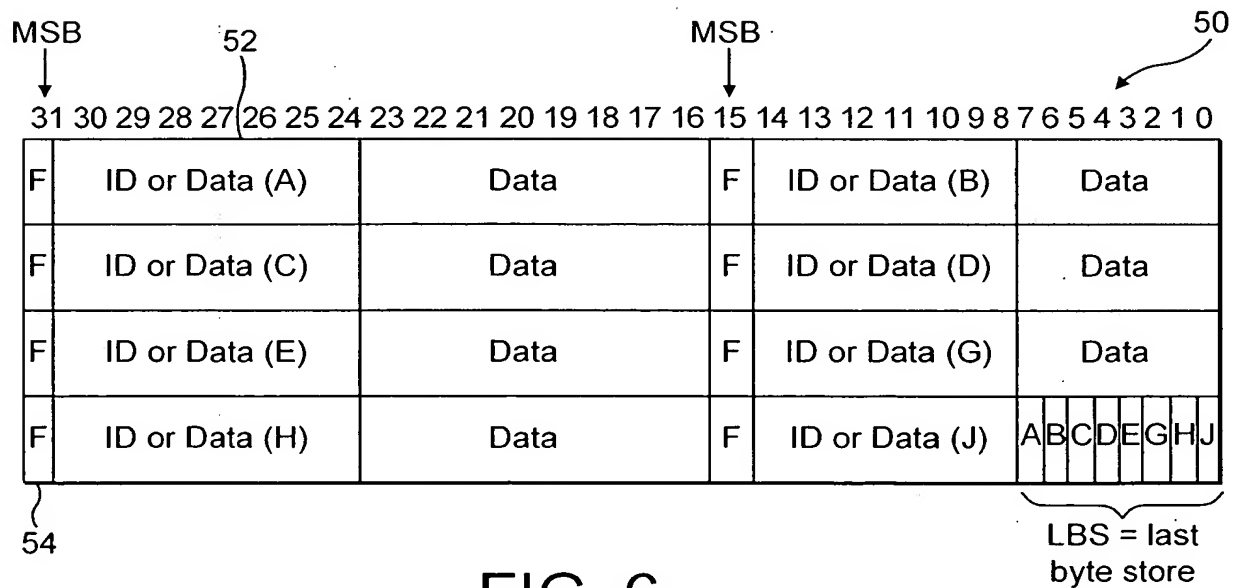


FIG. 6

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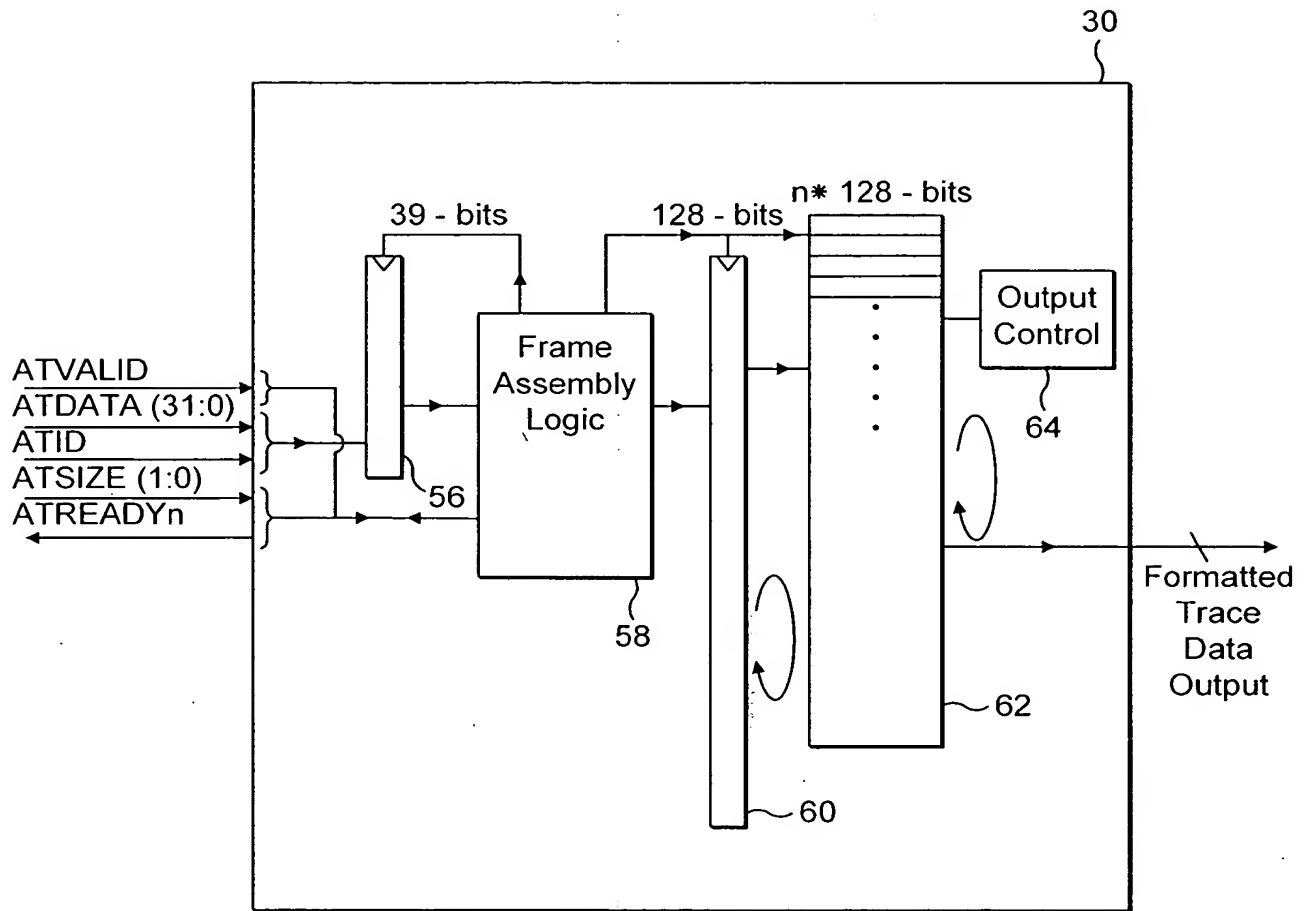


FIG. 7

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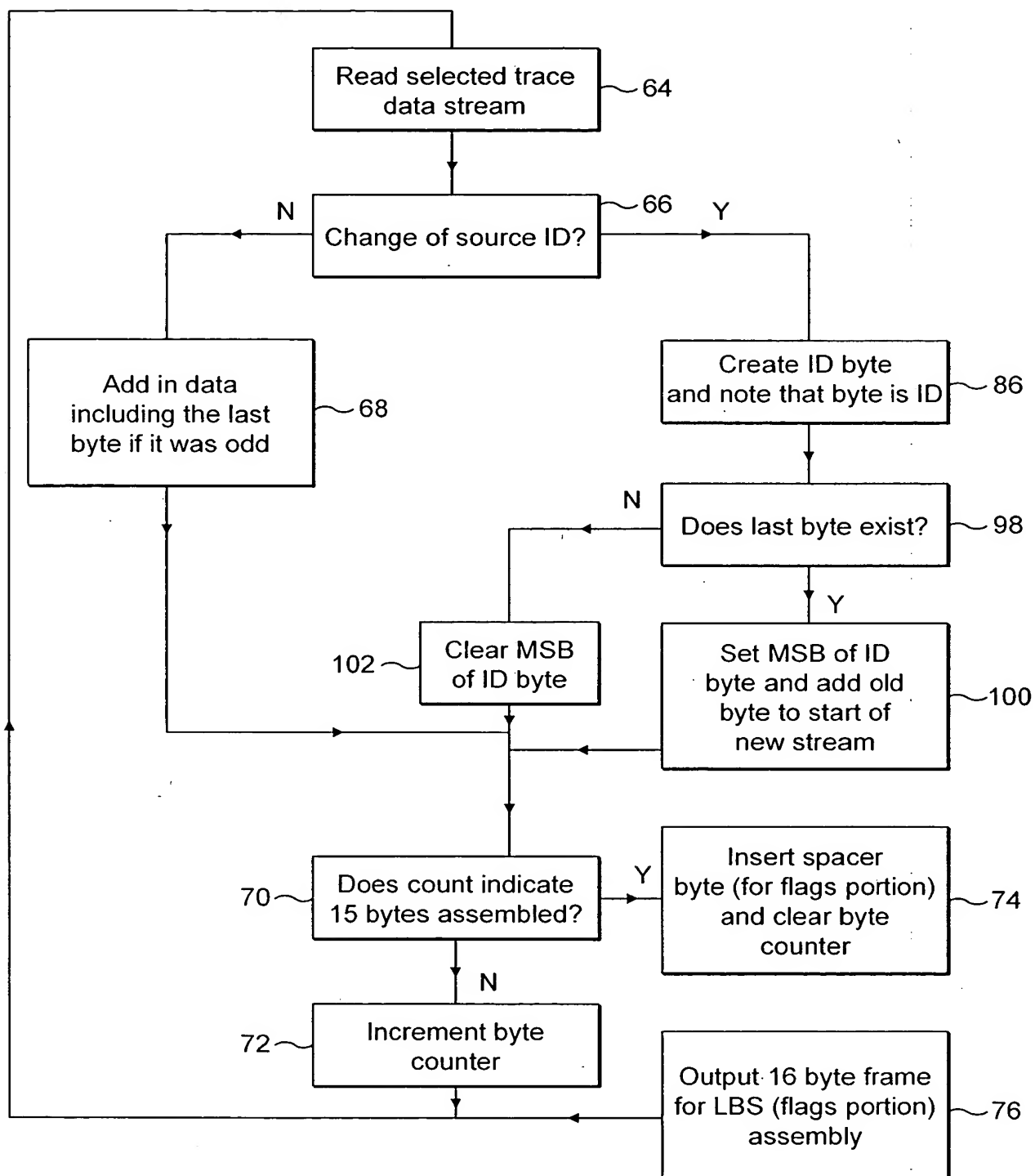


FIG. 8

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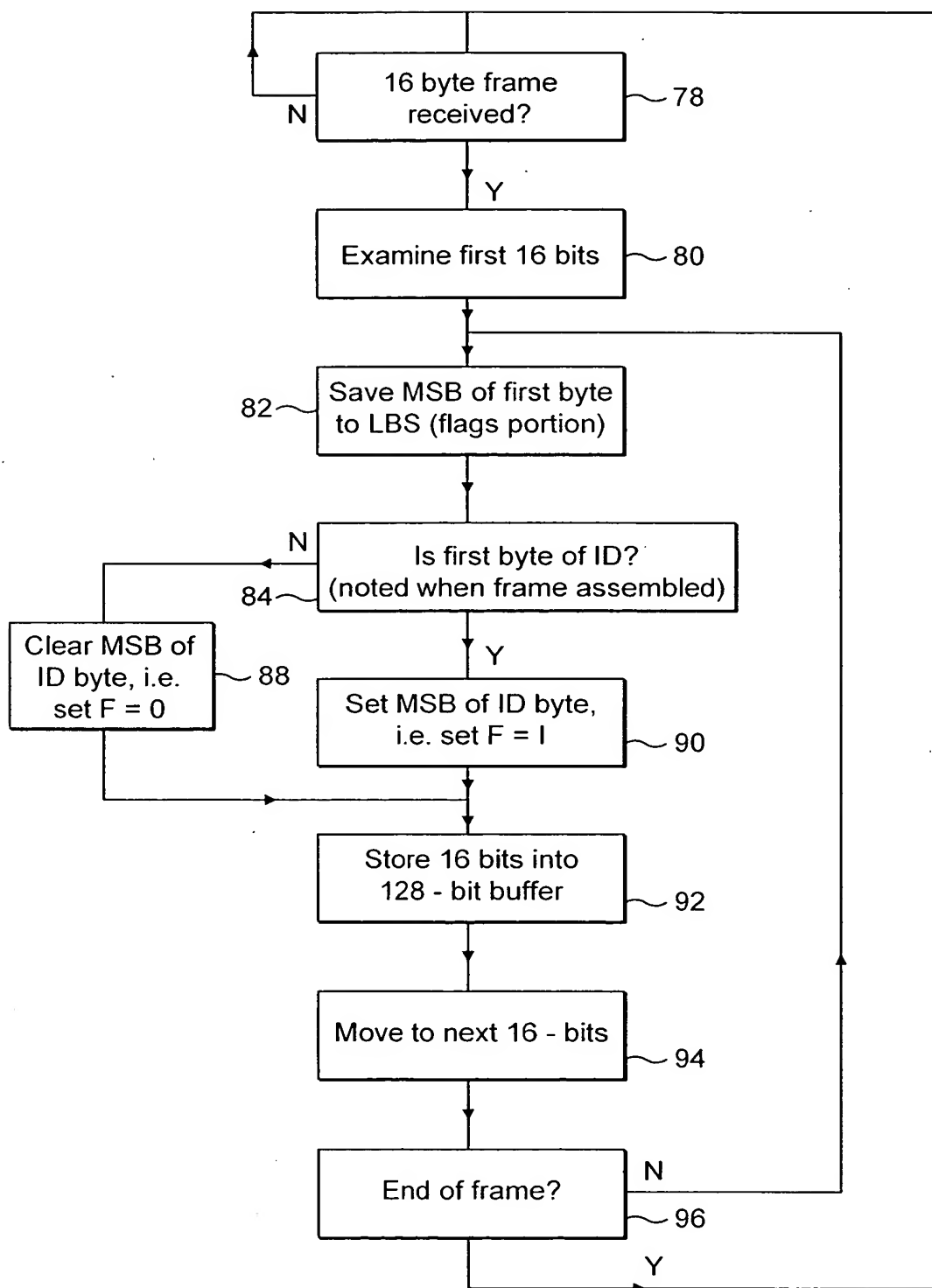


FIG. 9